BCW89-Q

60 V, 100 mA PNP general purpose transistor

8 January 2024 **Product data sheet**

1. General description

PNP switching transistor in a small SOT23 Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- Low current (max. 100 mA)
- Low voltage (max. 60 V)
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

- General purpose switching and amplification

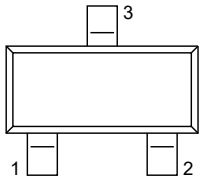
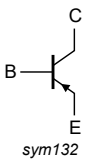
4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
V_{CEO}	collector-emitter voltage	open base; I_C = -2 mA		-	-	-60	V
I_C	collector current			-	-	-100	mA
h_{FE}	DC current gain	V_{CE} = -5 V; I_C = -10 µA; T_j = 25 °C		-	90	-	

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	B	base		
2	E	emitter		
3	C	collector		

SOT23

sym132

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
BCW89-Q	SOT23	plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	SOT23

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
BCW89-Q	H3%

[1] % = placeholder for manufacturing site code

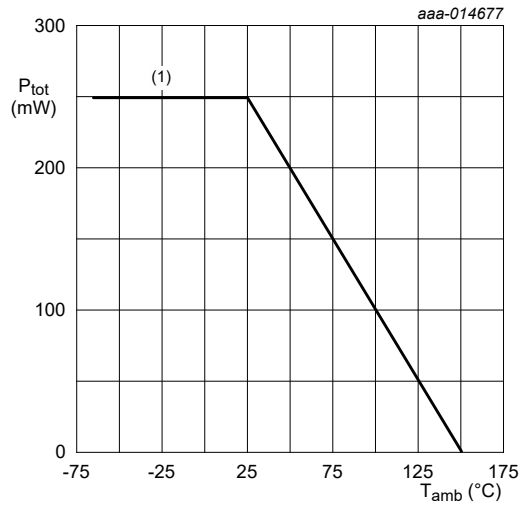
8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter		-	-80	V
V_{CEO}	collector-emitter voltage	open base; I_C = -2 mA		-	-60	V
V_{EBO}	emitter-base voltage	open collector		-	-5	V
I_C	collector current			-	-100	mA
I_{CM}	peak collector current	single pulse; $t_p \leq$ 1 ms		-	-200	mA
I_{BM}	peak base current			-	-200	mA
P_{tot}	total power dissipation	$T_{amb} \leq$ 25 °C	[1]	-	250	mW
T_j	junction temperature			-	150	°C
T_{amb}	ambient temperature			-65	150	°C
T_{stg}	storage temperature			-65	150	°C

[1] Transistor mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.



(1) FR4 PCB; standard footprint

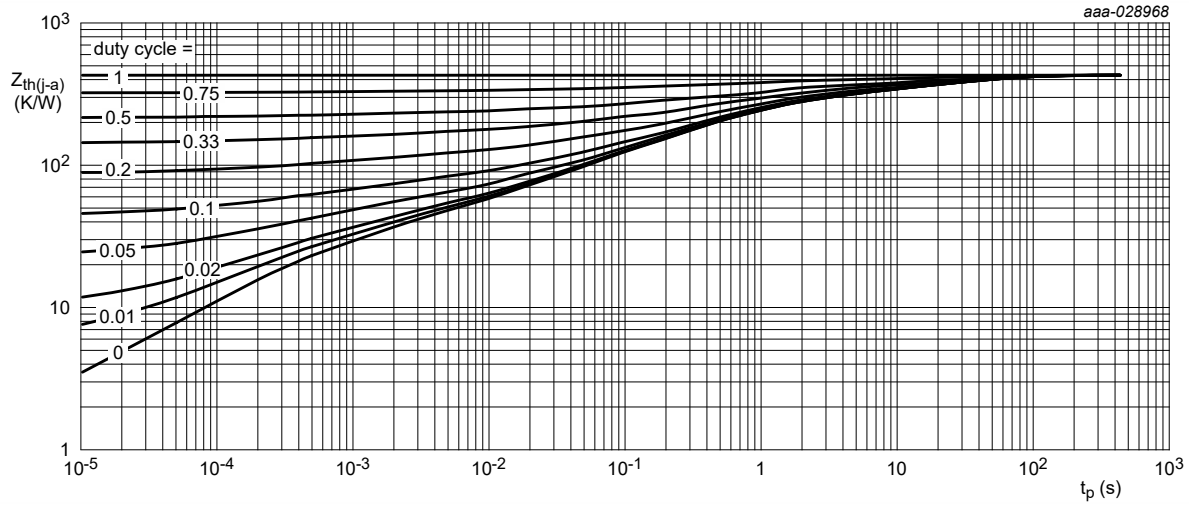
Fig. 1. Power derating curve

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	500	K/W

[1] Transistor mounted on an FR4 printed-circuit board, single-sided copper, tin-plated and standard footprint.



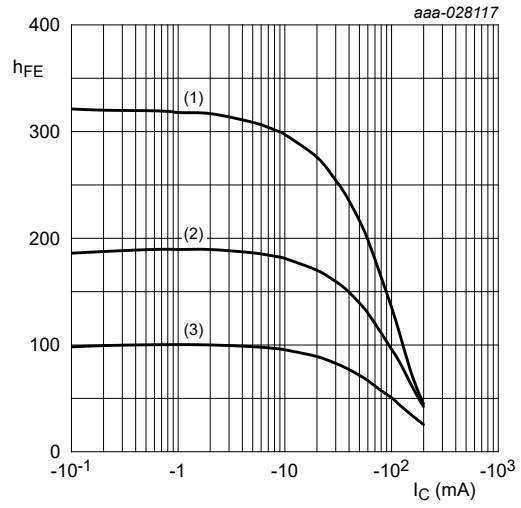
FR4 PCB, single-sided, 35 µm copper, tin-plated and standard footprint

Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

10. Characteristics

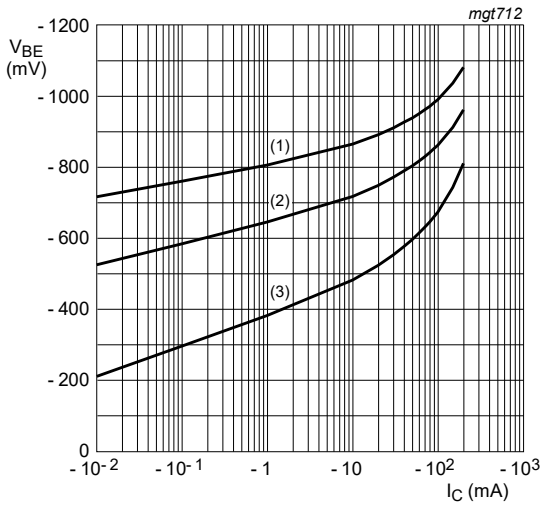
Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
I_{CBO}	collector-base cut-off current	V_{CB} = -20 V; I_E = 0 A; T_j = 25 °C		-	-	-100	nA
		V_{CB} = -20 V; I_E = 0 A; T_j = 100 °C		-	-	-10	µA
I_{EBO}	emitter-base cut-off current	V_{EB} = -5 V; I_C = 0 A; T_j = 25 °C		-	-	-100	nA
h_{FE}	DC current gain	V_{CE} = -5 V; I_C = -10 µA; T_j = 25 °C		-	90	-	
		V_{CE} = -5 V; I_C = -2 mA; T_j = 25 °C		120	-	260	
V_{CEsat}	collector-emitter saturation voltage	I_C = -10 mA; I_B = -0.5 mA; T_j = 25 °C		-	-80	-300	mV
		I_C = -50 mA; I_B = -2.5 mA; T_j = 25 °C		-	-150	-	mV
V_{BEsat}	base-emitter saturation voltage	I_C = -10 mA; I_B = -0.5 mA; T_j = 25 °C		-	-720	-	mV
		I_C = -50 mA; I_B = -2.5 mA; T_j = 25 °C		-	-810	-	mV
V_{BE}	base-emitter voltage	V_{CE} = -5 V; I_C = -2 mA; T_j = 25 °C		-600	-	-750	mV
C_c	collector capacitance	V_{CB} = -10 V; I_E = 0 A; i_e = 0 A; f = 1 MHz; T_j = 25 °C		-	4.5	-	pF
f_T	transition frequency	V_{CE} = -5 V; I_C = -10 mA; f = 100 MHz; T_j = 25 °C		-	150	-	MHz
NF	noise figure	V_{CE} = -5 V; I_C = -200 µA; R_S = 2 kΩ; f = 1 kHz; B = 200 Hz; T_j = 25 °C		-	-	10	dB



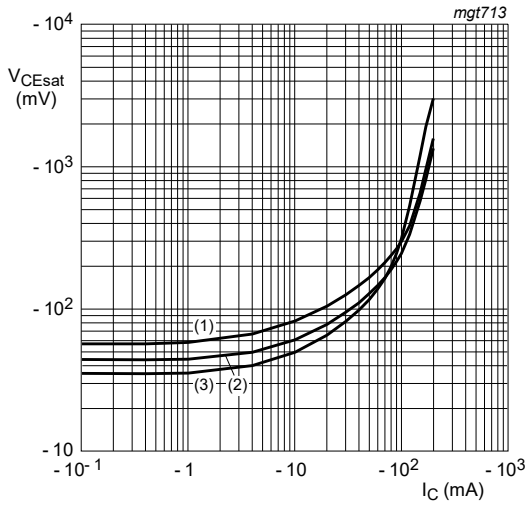
V_{CE} = -5 V
(1) T_{amb} = 150 °C
(2) T_{amb} = 25 °C
(3) T_{amb} = -55 °C

Fig. 3. DC current gain as a function of collector current; typical values



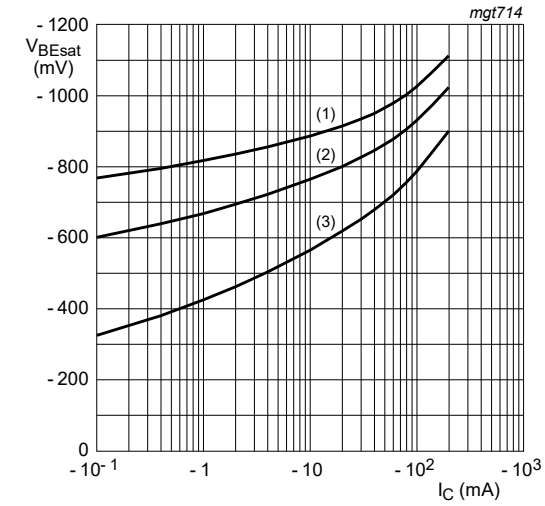
V_{CE} = -5 V
(1) T_{amb} = -55 °C
(2) T_{amb} = 25 °C
(3) T_{amb} = 150 °C

Fig. 4. Base-emitter voltage as a function of collector current; typical values



I_C/I_B = 20
(1) T_{amb} = 150 °C
(2) T_{amb} = 25 °C
(3) T_{amb} = -55 °C

Fig. 5. Collector-emitter saturation voltage as a function of collector current; typical values



I_C/I_B = 20
(1) T_{amb} = -55 °C
(2) T_{amb} = 25 °C
(3) T_{amb} = 150 °C

Fig. 6. Base-emitter saturation voltage as a function of collector current; typical values

11. Test information

Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline

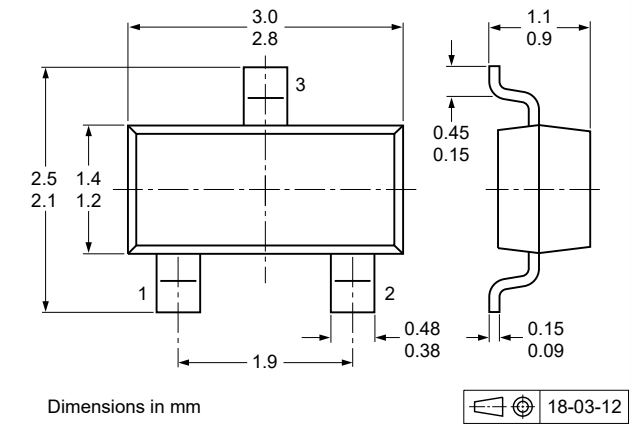


Fig. 7. Package outline SOT23

13. Soldering

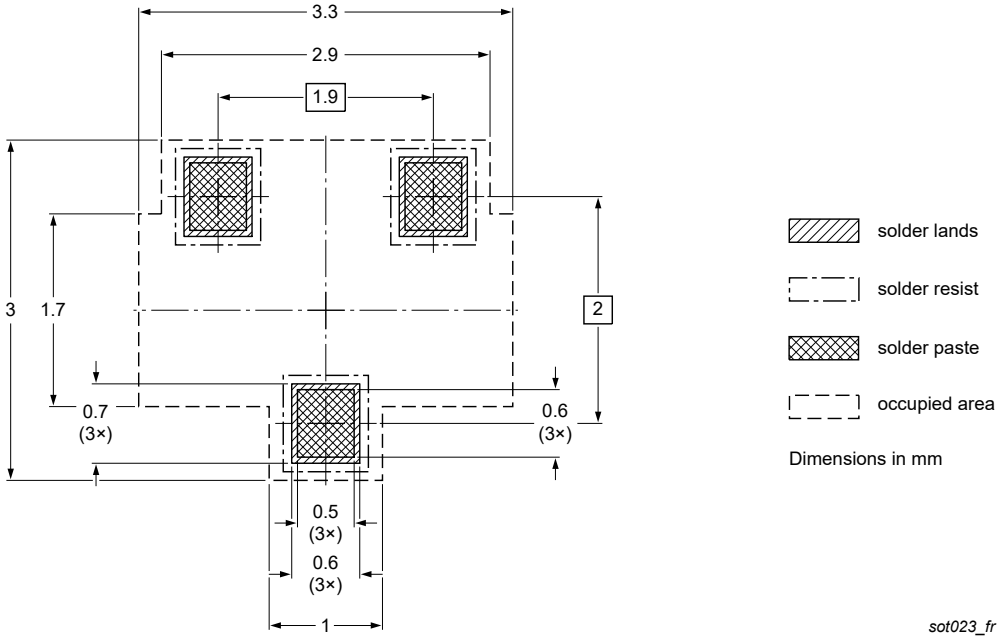


Fig. 8. Reflow soldering footprint for SOT23

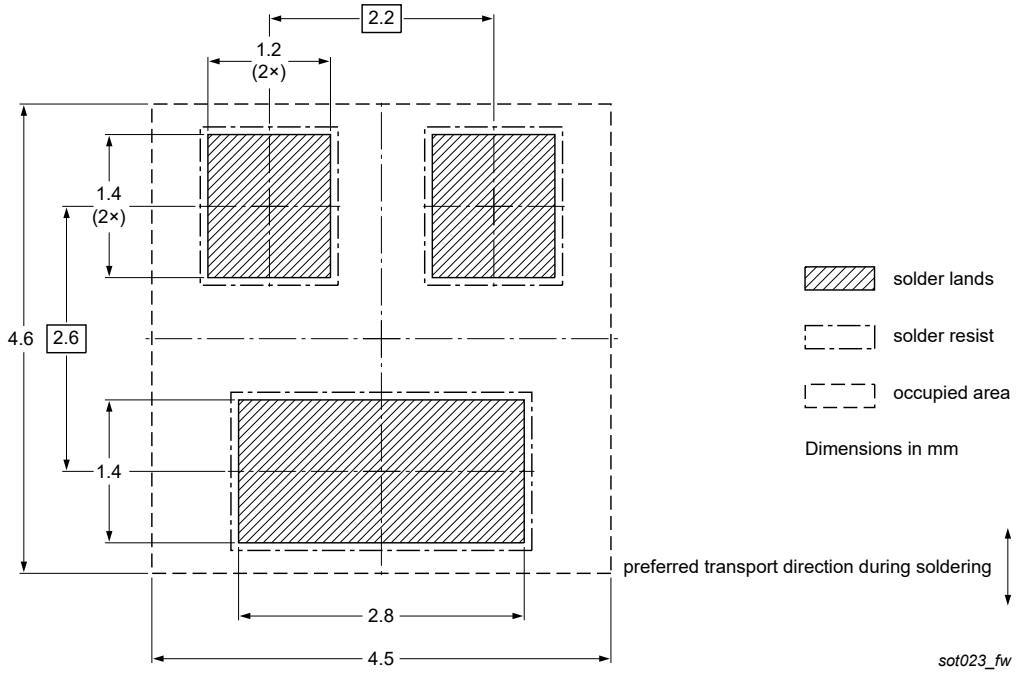


Fig. 9. Wave soldering footprint for SOT23

14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
BCW89-Q v.1	20240108	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.
[2] The term 'short data sheet' is explained in section "Definitions".
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Date of release: 8 January 2024